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(PTO ASSISTANCE)

Application : <u>10/796,164</u>	Examiner : <u>A. Wong</u>	GAU : <u>2635</u>
From : <u>L. MITCHELL</u>	Location : <u>IDC</u> FMF FDC	Date : <u>12/13/05</u>
Tracking # : <u>EPM 10/796,164</u>		Week Date : <u>10/31/05</u>

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449	_____	<input type="checkbox"/> Continuing Data
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<input type="checkbox"/> DRW	_____	
<input type="checkbox"/> OATH	_____	
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<input checked="" type="checkbox"/> SPEC	<u>3/10/04</u>	

[RUSH] MESSAGE: SERIAL NUMBER MISSING ON PAGE 18, LINE 8
OF SPECIFICATION.

THANK YOU
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 REV 10/04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Confirmation No. 3037

ELY et al

Allowed: October 24, 2005

Serial No. 10/796,164

Atty Ref.: 1179-55

Filed: March 10, 2004

TC/A.U.: 2635

For: POSITION DETECTOR

Examiner: A. Wong

* * * * *

January 6, 2006

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

VIA FACSIMILE 571-273-9009**AND/OR 703-308-6642**

Attention: Rori Burch

VIA EMAIL: Rori.burch@uspto.gov**RESPONSE TO NOTICE TO
FILE CORRECTED APPLICATION PAPERS**

Sir:

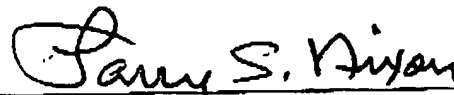
In response to the Notice to File Corrected Application Papers mailed 12/28/2005, and in conjunction with concurrent payment of issue fees, the serial number information missing from the specification at page 18, line 8 is:

"PCT GB98/01557 (WO 98/54545)"

Respectfully submitted,

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**UNITED STATES PATENT AND TRADEMARK OFFICE**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Serial Number
10796164

Date Mailed
12/28/05

NOTICE TO FILE CORRECTED APPLICATION PAPERS***Notice of Allowance Mailed***

This application has been accorded an Allowance Date and is being prepared for issuance. The application, however, is incomplete for the reasons below.

Applicant is given 30 days from the mail date of this Notice within which to correct the informalities indicated below. A failure to reply will result in the application being ABANDONED. This period for reply is NOT extendable under 37 CFR 1.136 (a) or (b).

- ♦ Specification page 16, line 8 serial number missing. Fax missing information to number below or e-mail.
 - o For status updates visit <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR System, contact the Electronic Business Center (EBC) toll free at 866-217-9197.

APPLICANT MUST SUPPLY MISSING INFORMATION WITHIN 30 DAYS OF THE MAIL DATE OF THIS NOTICE.

A copy of this notice **MUST** be returned with the reply. Please address response to Commissioner for Patents P.O. Box 1450
Alexandria, VA 22313-1450

A handwritten signature in black ink, appearing to read "Rori Burch", followed by a horizontal line.

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Long Island, New York, USA. The technique has existed for at least 20 years and the general principle and structure of a suitable wire bonding apparatus is described in, for example, US 4693778, the contents of which are incorporated herein by reference.

PCT/GB98/01557 (WO 98/54345)
 The applicant's copending International Application No. filed on 28 May 1998 describes the way in which such a wire bonding technique can be used to manufacture windings for use in position sensors. More specifically, the windings are formed by bonding an enamelled copper wire onto a suitable substrate in the required pattern. In this embodiment, the eight windings of the digitising tablet 9 are formed on a separate substrate which are then superimposed on top of each other to form a multi layered structure. More specifically, in this embodiment, the layered structure is formed by firstly winding the wire onto a wiring loom (not shown) in the required pattern in order to form a first one of the eight windings. This winding is then sandwiched between first and second substrates to trap the wires in place. Another winding is then created using the wiring loom and then sandwiched between the second substrate and a third substrate. This process is then repeated until all eight windings have been sandwiched between two substrates.

Figure 4e shows a cross-sectional view along the X axis of the digitising tablet 9 shown in Figure 1. As shown, there are nine substrate layers 45-1 to 45-9 which sandwich the eight separate windings 41-1 to 41-8. The top substrate layer 45-1 also acts as a protective layer which may have printed material on the top surface depending on the application for the X-Y digitising tablet. As shown, in this embodiment, the windings for the X position measurement are arranged in alternating